

Instruction Fetch 3	\$01C
Instruction Fetch 1	\$018
Instruction Fetch 1	\$014
Instruction Fetch 0	\$010
	\$00C
	\$008
TPU Status	\$004
TPU Master	\$000

Program Counter (PC)	\$11C
Control (C)	\$118
General Register 1 (G1)	\$114
General Register 0 (G0)	\$110
Transfer Count (TC)	\$10C
Transfer Destination (TD)	\$108
Transfer Source (TS)	\$104
Instruction Buffer (IB)	\$100